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Institute of Parallel and Distributed Systems (IPVS) Distributed Systems Group

# Minimizing Communication Overhead in Window-Based Parallel Complex Event Processing

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## **Motivation**

- Timely reactions to situations in the surrounding world
  - Algorithmic trading, Internet of Things,...
  - Sensors gather low-level information
  - Complex Event Processing (CEP) operator networks detect events
- Big Data new challenges for CEP
  - High event rates
  - Parallelization needed for operators







# **Example: Face Recognition Operator**

- Is a person of interest in the video stream?
- Query: Aperiodic(A; B; C) with

   A → <type = requested\_person, time = t>
   B → <type = face, "face\_match(A)">
   C → time ≥ t + time frame
- Window-based query





## **Operator Parallelization**

#### Data parallelization





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## **Scheduling Problem**



- Batching: Scheduling of subsequent overlapping window to the same operator instance
  - $\rightarrow$  reduced network load
  - $\rightarrow$  more computational load on single instances  $\rightarrow$  higher latency

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Scheduling problem: Maximize batching to an operator instance, while a given latency limit is still kept



## **Contributions**

- Problem analysis
  - Key factors of operator latency at overlapping windows
- Approach: model-based batch scheduling controller
  - Latency model
  - Scheduling algorithms
- Evaluations show efficacy and low overhead of the controller



## Challenge

Long feedback delays of control loop:



Unknown events of the window at scheduling time



## Implications

- Reactive scheduling?
  - "Schedule **b** windows, measure latency peak, adapt scheduling"
    - $\rightarrow$  State-of-the-art in stream processing
  - DCEP problem: schedule **open windows** 
    - $\rightarrow$  Very long feedback delays to capture implications of scheduling
- Offline trained blackbox latency model?
  - The parameters are too many and the relation is complex
    - $\rightarrow$  Operator-specific
    - $\rightarrow$  Hard to predict outside of trained parameter ranges
- Our approach: Model-based scheduling controller



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## **Approach Idea**

#### Total operational latency of an event: queueing + processing latency

$$\lambda_{o}(e) = \lambda_{q}(e) + \lambda_{p}(e)$$

→ queuing dominates operational latency →  $\lambda_{\alpha}(e)$  depends on  $\lambda_{p}$  and *iat* of previous events

Idea: Predict the queuing latency peak

Approach:

- 1) Predict the set of events in w<sub>new</sub>
- 2) Predict the impact of that set on the latency peak



## **Gain of Events**

- Event e has processing latency  $\lambda_p$  and inter-arrival time *iat*
- If  $\lambda_p > iat$ , successor event has more queuing latency  $\lambda_q$
- If  $\lambda_p < iat$ , successor event has smaller or zero  $\lambda_q$
- Difference between  $\lambda_p$  and *iat*  $\rightarrow$  gain:  $\gamma(e) = \lambda_p(e) iat$





#### **Sequence of Gains**

Worst Case / Medium Case / Best Case



- Generally:  $\lambda_q^{max} = \Gamma^- + \alpha * \Gamma^+, \alpha \in [0, 1]$ 
  - $\alpha$  is termed the *compensation factor*
  - $\rightarrow$  the extent of interleaving of negative and positive gains

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#### **Predictions**

Predict...

- ...total negative and positive gains
- $\rightarrow$  depends on events' processing latency and *iat*

$$\gamma(e) = \lambda_p(e) - iat$$

- ...initial queuing latency
- $\rightarrow$  feedback from operator instances
- ...compensation factor
- $\rightarrow$  conservative heuristics, or expert knowledge



#### **Inter-Arrival Time**

Distribution of *iat* in n equally-sized bins



## **Processing Latency**

•  $\lambda_p$  depends on overlap  $\theta$  and proc. latency  $\lambda_p^w$  in single window  $\Rightarrow \lambda_p = \theta * \lambda_p^w$ 

- $\lambda_p^w$  depends on event type and position
  - Prediction of  $\lambda_p^w$  dependent on type
  - Prediction of set of events dependent on type



### **Overlap**

- All events are predicted to have mean overlap  $\overline{\theta}$
- Model: Weighted average based on current window shift Δ and window scope ws





## **Evaluations: Setup**

- We perform all experiments on a computing cluster consisting of 16 physical hosts with 8 cores
  - Intel(R) Xeon(R) CPU E5620 @ 2.40GHz
  - 24 GB memory
  - 10-Gigabit-Ethernet connections
- Components of the data parallelization framework are evenly distributed among the available hosts



## **Evaluations: Scenarios**

- Traffic monitoring operator
  - Overtaking detection



- Face recognition operator
  - Is a person of interest in the video stream?





#### **Evaluations: Negative and Positive Gains**



Model is sufficiently accurate and precise



## **Evaluations: Compensation Factor**



Compensation factor can fine-tune the model

#### **Evaluations**



- Model-based controller keeps the latency bounds
- Reactive controller violates them

## **Evaluations: Communication Overhead**



Significant reduction of communication overhead!



## **Evaluations: Model Overhead**



- Scheduling very fast  $\rightarrow$  no bottleneck
- Updating model statistics reasonably fast

## Conclusion

- Window-based data parallelization in DCEP poses scheduling tradeoff:
  - Replicate or batch an overlapping window?
- Trade-off is hard to control
- Approach: model-based batch scheduling controller
- Evaluations show the proposed approach saves bandwidth and keeps latency bounds



#### **End of Presentation**

Time for questions and answers.





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